## **CS 3733 Virtual Memory Problem**

1) Assume a machine uses 33-bit virtual addresses and 29-bit physical addresses and is byte-addressable. It uses a 2-level page table with a page size of 512 bytes. All page tables have 32-bit entries. The top level page table entries contain physical addresses. The second level page table entries contain a frame number, a valid bit and 5 bits for protection. The top level

page tables and second level page tables are the same size. Give the best answer to each of the following that you can. If you need to make additional assumption state the assumptions you are making. If the answer is a power of 2, leave it in exponential form.	ons to give an answer
a) How many bits of a virtual address are used for the page offset?	a)
b) How many bits of a physical address are used for the frame offset?	b)
c) How many bits of a virtual address are used to specify the top level page table entry?	c)
d) How many bits of a virtual address are used to specify the second level page table entry?	d)
e) What is the size in bytes of a top level page table?	e)
f) How many pages of memory does one top level page table require?	f)
g) How many bits are available in a physical address for the frame number?	g)
<ul><li>h) What is the maximum number of bytes of physical memory that this machine can have?</li><li>i) If all of the page tables of a single process were in physical memory,</li></ul>	h)

how many bytes would this take up?